



Docket No.: PEK-IN1022

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By: [Signature] Date: May 16, 2002

Inventor : Achim Gratz
 Applic. No. : 09/940,001
 Filed : August 27, 2001
 Title : Vertical Non-Volatile Semiconductor Memory Cell and Method for Manufacturing the Memory Cell
 Examiner : Richard A. Booth - Art Unit: 2812

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RESPONSE

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated April 23, 2002, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicant elects group I, claims 1-13 for prosecution at this time.

The applicant reserves the right to file a divisional application including the method claims of group II at a later date and/or to request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims is solicited.

Respectfully submitted,

[Signature]
For Applicant

ENCLOSURE
REG. NO. 37,000

Date: May 16, 2002

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